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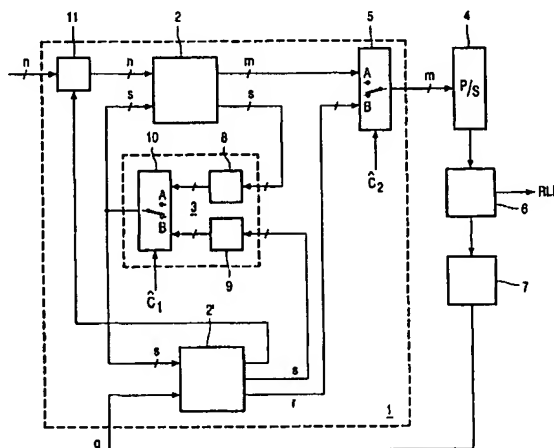
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(54) Title: **DEVICE FOR ENCODING A STREAM OF DATABITS OF A BINARY SOURCE SIGNAL INTO A STREAM OF DATABITS OF A BINARY CHANNEL SIGNAL, MEMORY MEANS, DEVICE FOR RECORDING INFORMATION, RECORD CARRIER, DEVICE FOR CODING AND DEVICE FOR PLAYING BACK**



(57) Abstract: In a device for encoding a stream of databits of a binary source signal into a stream of databits of a binary channel signal the bitstream of the source signal is divided into n-bit source words. The device comprises converting means adapted to convert a block of p consecutive n-bit source words into a corresponding block of p consecutive m-bit channel words, such that the conversion for at least most of the n-bit source words is parity preserving and/or parity inverting, where $m \geq n \geq 1$. The converting means comprise memory means which contain for each n-bit source word a number of m-bit channel words, arranged in coding states, and a corresponding state number, indicating the state for a next m-bit channel word. After each block of source words q dc-control sourcebits are added, which are converted into r dc-control channelbits, independent of the conversion of a following source word.

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